

R20CE

SD-SDI to Component and Composite Analog Converter, 10-bit

Features at a glance

- Excellent-Quality 10-bit Universal D/A Conversion
- Full 10-bit Data path, 4x Oversampling
- SD-SDI Input, 2 Re-clocked, Loop-Thru SDI Outputs
- Simultaneous Component and Composite Analog Outputs
- YPbPr, Betacam, or RGB Component Formats
- NTSC or PAL Composite Formats
- Digital Noise Reduction
- Optional Frame Synchronizer Allows Genlock to Reference, Full Timing Adjustment

Tech specs

Input:

- SD-SDI (SMPTE 259M), 1 x BNC

Reference:

- Passive loop, 2 x BNC

Outputs:

- (Simultaneous Component and Composite output)
- YPbPr - SMPTE
- EBU-N10
- Betacam
- RGB
- NTSC
- PAL
- Y/C (S-Video)
- 3 x BNC
- NTSC/PAL or Sync
- 1 x BNC
- Re-clocked loop-thru SDI
- 2 x BNC

D/A Converters:

- 10-bits
- 4x oversampling
- Clock Jitter Filtering to 2.5Hz

Frequency Response:

- Y +/- .15 dB to 5.5MHz
- C +/- .15 dB to 2.5MHz (Component)
- C +/- .15 dB to 1.3MHz (Composite)
- Less than .5% K Factor (2T)

User Controls:

- Output Video Format
- Pedestal On/Off
- Narrow/Wide Blanking
- Digital Noise Reduction
- Output Timing Adj.
- (w/Frame Sync option)

Physical:

- Fits AJA R-Series Frames
- Compatible with Leitch 6800 Series Frames

Power:

- 7 watts (8 watts w/Frame Sync option)



The R20CE SD-SDI to Analog Video Converter provides excellent-quality 10-bit conversion of SD-SDI to both component and composite video formats simultaneously. The 4 analog outputs are user configurable to NTSC/PAL, Y/C (S-Video), YPbPr (SMPTE, EBU-N10), Betacam, or RGB. The component and composite outputs are completely independent including optimum chroma filtering for each and independent pedestal configuration. The R20CE also features an exclusive PLL jitter filter/memory to reduce the effects of SDI jitter on the output analog video. This feature, along with the precision 4x oversampled D/A filters, provides the highest-quality analog outputs - including very low phase noise in composite outputs. The optional FSG (Frame Sync/Genlock) Module allows genlock to an external reference with full timing adjustment. Without the FSG Module, the reference input provides color frame timing.

